

Process C3025 CMOS 3μm 10 Volt Analog

Electrical Characteristics

				T=25°C	Unless of	therwise noted
N-Channel Transistor	Symbol	Minimum	Typical	Maximum	Unit	Comments
Threshold Voltage	VT _N	0.65	0.85	1.05	V	100x4µm
Body Factor	γ _N		0.87		V ^{1/2}	100x4µm
Conduction Factor	β _N	40	48	56	μA/V ²	100x100µm
Effective Channel Length	Leff _N	3.05	3.40	3.75	μm	100x4µm
Width Encroachment	ΔW_N		0.550		μm	Per side
Punch Through Voltage	BVDSS _N	16.5			V	
Poly Field Threshold Voltage	VTF _{P(N)}	12			V	

P-Channel Transistor	Symbol	Minimum	Typical	Maximum	Unit	Comments
Threshold Voltage	VTP	-0.7	-0.9	-1.1	V	100x4µm
Body Factor	γ _P		0.75		V1/2	100x4µm
Conduction Factor	β _P	13	16	19	μA/V ²	100x100µm
Effective Channel Length	Leff _P	3.00	3.35	3.70	μm	100x4µm
Width Encroachment	ΔW_P		0.8		μm	Per side
Punch Through Voltage	BVDSS _P	-16.5			V	
Poly Field Threshold Voltage	VTF _{P(P)}	-12			V	

Diffusion & Thin Films	Symbol	Minimum	Typical	Maximum	Unit	Comments
Well (field) Sheet Resistance	ρ _{P-well(f)}	3.25	5.25	7.25	KΩ/□	P-well
N+ Sheet Resistance	ρ _{N+}	13	20	27	Ω/□	
N+ Junction Depth	X _{jN+}		0.8		μm	
P+ Sheet Resistance	ρ _{P+}	50	80	100	Ω/□	
P+ Junction Depth	X _{jP+}		0.7		μm	
Gate Oxide Thickness	T _{GOX}	45	48	51	nm	
Interpoly Oxide Thickness	T _{P1P2}	56	66	76	nm	
Gate Poly Sheet Resistance	ρ_{POLY1}	15	22	30	Ω/□	
Bottom Poly Sheet Res.	ρ_{POLY2}	15	22	30	Ω/□	
Metal-1 Sheet Resistance	ρ _{M1}		50		mΩ/□	
Passivation Thickness	TPASS		200+900		nm	oxide+nit.

Capacitance	Symbol	Minimum	Typical	Maximum	Unit	Comments
Gate Oxide	Cox	0.68	0.72	0.78	fF/μm²	
Metal-1 to Poly-1	C _{M1P}	0.047	0.0523	0.0575	fF/μm²	
Metal-1 to Silicon	C _{M1S}	0.027	0.30	0.034	fF/μm²	
Poly-1 to Poly-2	C _{P1P2}	0.453	0.523	0.617	fF/μm²	

Process C3025

Starting Material	P <100>	N+/P+ Width/Space	3.0 / 3.0µm	
Starting Mat. Resistivity	15 - 25 Ω-cm	N+ To P+ Space	12µm	
Typ. Operating Voltage	10V	Contact To Poly Space	2.5µm	
Well Type	P-well	Contact Overlap Of Active	1.5µm	
Metal Layers	1	Contact Overlap Of Poly	1.0µm	
Poly Layers	2	Metal-1 Overlap Of Contact	1.0µm	
Contact Size	2.0x2.0µm	Minimum Pad Opening	100x100µm	
Metal-1 Width/Space	3.5 / 2.5µm	Minimum Pad-to-Pad Spacing	5.0µm	
Gate Poly Width/Space	3.0 / 3µm	Minimum Pad Pitch	80.0µm	

Physical Characteristics

Special Feature of C3025 Process: 10 Volt P-well single metal analog process.